

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**In re U.S. Patent Application of**

**YAMAMOTO et al.**

**Application Number: 10/817,004**

**Filed: April 5, 2004**

**For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE  
AND MANUFACTURING METHOD THEREOF**

**Attorney Docket No. HITA.0536**

**Honorable Assistant Commissioner  
for Patents  
Washington, D.C. 20231**



**Art Unit 2826**

**Examiner Ahmed N. Sefer**

**AMENDMENT AND RESPONSE**

Dear Sir:

This is in response to the Office Action mailed on November 16, 2005, the period of response to which is set to expire on May 16, 2005, with the payment of a three-month extension fee submitted herewith. Please amend the above-referenced application as follows: